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- A digital signal is a voltage in function of time
 - Digital signals are meant to carry two possible values, called 0 and 1, but they may have noise, glitches, ringing, and other undesirable effects
- A digital gate has input and output signals
 - The output signal is slightly delayed with respect to the input
- We will model gates as agents and signals as streams
 - This assumes perfectly clean signals and zero gate delay
 - We will later add a delay gate in order to model gate delay













GateMaker is analogous to a generic class or metaclass

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79

- FunG is analogous to a class
- A running gate is analogous to an object




























































































































































































































































































































































































































































































































